

10016863_CLS

Most Frequently Occurring Classifications of Patents Returned
From A Search of 10016863 on March 29, 2004

Original Classifications

6 365/200
4 365/201
3 714/719
2 710/26
2 712/42
2 714/718

Cross-Reference Classifications

5 365/200
5 365/201
5 714/718
4 365/230.06
2 365/225.7
2 365/230.08
2 365/238.5
2 710/22
2 710/28
2 710/36
2 710/37
2 711/206
2 712/228
2 714/3
2 714/4
2 714/42
2 714/43
2 714/56
2 714/711
2 714/719

Combined Classifications

11 365/200
9 365/201
7 714/718
5 365/230.06
5 714/719
3 365/225.7
3 714/42
3 714/711
2 365/230.08
2 365/238.5
2 710/22
2 710/26
2 710/28

10016863_CLS

2 710/36
2 710/37
2 711/203
2 711/206
2 712/216
2 712/217
2 712/218
2 712/228
2 712/42
2 714/3
2 714/4
2 714/43
2 714/56
2 714/710
2 714/733
2 714/736
2 714/743

10016863_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 10016863 on March 29, 2004

- 11 365/200 (6 OR, 5 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/200 .Bad bit

- 9 365/201 (4 OR, 5 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/201 .Testing

- 7 714/718 (2 OR, 5 XR)
 - Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
 - 714/699 PULSE OR DATA ERROR HANDLING
 - 714/718 .Memory testing

- 5 365/230.06 (1 OR, 4 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/230.01 ADDRESSING
 - 365/230.06 .Particular decoder or driver circuit

- 5 714/719 (3 OR, 2 XR)
 - Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
 - 714/699 PULSE OR DATA ERROR HANDLING
 - 714/718 .Memory testing
 - 714/719 ..Read-in with read-out and compare

- 3 365/225.7 (1 OR, 2 XR)
 - Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 - 365/189.01 READ/WRITE CIRCUIT
 - 365/225.7 .Having fuse element

- 3 714/42 (1 OR, 2 XR)
 - Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
 - 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
 - 714/1 .Reliability and availability
 - 714/25 ..Fault locating (i.e., diagnosis or testing)
 - 714/40 ...Component dependent technique
 - 714/42Memory or storage device component fault

10016863_CLSTITLES

- 3 714/711 (1 OR, 2 XR)
 Class 714 : ERROR DETECTION/CORRECTION AND FAULT
 DETECTION/RECOVERY
 714/699 PULSE OR DATA ERROR HANDLING
 714/710 .Replacement of memory spare location, portion
 or segment
 714/711 ..Spare row or column
- 2 365/230.08 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/230.01 ADDRESSING
 365/230.08 .Including particular address buffer or latch
 circuit arrangement
- 2 365/238.5 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/230.01 ADDRESSING
 365/238.5 .Byte or page addressing
- 2 710/22 (0 OR, 2 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/22 .Direct Memory Accessing (DMA)
- 2 710/26 (2 OR, 0 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/22 .Direct Memory Accessing (DMA)
 710/26 ..Using addressing
- 2 710/28 (0 OR, 2 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/22 .Direct Memory Accessing (DMA)
 710/28 ..With access regulating
- 2 710/36 (0 OR, 2 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/36 .Input/Output access regulation
- 2 710/37 (0 OR, 2 XR)

10016863_CLSTITLES

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
PROCESSING SYSTEMS: INPUT/OUTPUT

710/1 INPUT/OUTPUT DATA PROCESSING

710/36 .Input/Output access regulation

710/37 ..Access dedication

2 711/203 (1 OR, 1 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY

711/200 ADDRESS FORMATION

711/202 .Address mapping (e.g., conversion,
translation)

711/203 ..Virtual addressing

2 711/206 (0 OR, 2 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: MEMORY

711/200 ADDRESS FORMATION

711/202 .Address mapping (e.g., conversion,
translation)

711/203 ..Virtual addressing

711/206 ...Translation tables (e.g., segment and page
table or map)

2 712/216 (1 OR, 1 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING

712/216 DYNAMIC INSTRUCTION DEPENDENCY CHECKING,
MONITORING OR CONFLICT RESOLUTION

2 712/217 (1 OR, 1 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING

712/216 DYNAMIC INSTRUCTION DEPENDENCY CHECKING,
MONITORING OR CONFLICT RESOLUTION

712/217 .Scoreboarding, reservation station, or
aliasing

2 712/218 (1 OR, 1 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING

712/216 DYNAMIC INSTRUCTION DEPENDENCY CHECKING,

10016863_CLSTITLES

MONITORING OR CONFLICT RESOLUTION

712/218 .Commitment control or register bypass

2 712/228 (0 OR, 2 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

712/220 PROCESSING
712/228 PROCESSING CONTROL
.Context preserving (e.g., context swapping,
checkpointing, register windowing)

2 712/42 (2 OR, 0 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

712/1 PROCESSING
712/32 PROCESSING ARCHITECTURE
.Microprocessor or multichip or multimodule
processor having sequential program contro

1

712/42 ..Operation

2 714/3 (0 OR, 2 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability
714/2 ..Fault recovery
714/3 ...By masking or reconfiguration

2 714/4 (0 OR, 2 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability
714/2 ..Fault recovery
714/3 ...By masking or reconfiguration
714/4Of network

2 714/43 (0 OR, 2 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY
714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability

10016863_CLSTITLES

714/25 ..Fault locating (i.e., diagnosis or testing)

714/40 ...Component dependent technique

714/43Bus, I/O channel, or network path componen

t

fault

2 714/56 (0 OR, 2 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability

714/48 ..Error detection or notification

714/55 ...Timing error (e.g., watchdog timer time-out

)

714/56Bus or I/O channel device fault

2 714/710 (1 OR, 1 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/699 PULSE OR DATA ERROR HANDLING

714/710 .Replacement of memory spare location, portion

,

or segment

2 714/733 (1 OR, 1 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/699 PULSE OR DATA ERROR HANDLING

714/724 .Digital logic testing

714/733 ..Built-in testing circuit (BILBO)

2 714/736 (1 OR, 1 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/699 PULSE OR DATA ERROR HANDLING

714/724 .Digital logic testing

714/736 ..Device response compared to expected
fault-free response

2 714/743 (1 OR, 1 XR)

Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/699 PULSE OR DATA ERROR HANDLING

714/724 .Digital logic testing

714/738 ..Including test pattern generator

714/743

10016863_CLSTITLES
...Addressing